

REMARKS

In the Office Action dated July 9, 2004, the Examiner objected to the disclosure of the present application because of certain formalities. However, Applicant respectfully disagrees with the Examiner's indication of grammatical errors cited therein. The Examiner is directed to the Preliminary Amendment which was filed in this case on January 4, 2001. Referring to page 2, line 12, it was noted that "On page 2, in line 7, replace "must no longer" with --no longer must--;"

In addition, on page 2, line 18 of the Preliminary Amendment, it was noted that "in line 4, change "Figure 3-7" to --Figures 3-7 show--;"

Thus, Applicant respectfully submits that the grammatical errors cited by the Examiner were correctly handled via the Preliminary Amendment filed in this case.

Applicant is also submitting herewith replacement sheets 1-3 (including Figures 1-7) which are intended to replace the previously-submitted drawings which included marked-up copies of Figures 1 and 4-7.

Claims 5-8 remain in this application. Claims 1-4 have been canceled. Claims 5-8 are being submitted at this time for clarification purposes only. No new matter has been added thereby.

The Examiner rejected, in particular, independent claim 1 of the present application under 35 U.S.C. §102(e) as being anticipated by Davidson et al. (U.S. Patent No. 5,774,540). For the following reasons, Applicant respectfully traverses the Examiner's rejection and respectfully requests the withdrawal thereof.

Specifically, Applicant submits that the Davidson reference does not teach or suggest the following claimed element of the present invention:

a memory table in the microprocessor, the memory table including catch-words with each catch-word having an option allocated thereto, wherein the catch-words may be selected by the user via the input device, with a respective option being immediately at least one of called and modified after the respective catch-word has been selected.

While the Davidson reference may disclose a "data memory," such data memory clearly does not contain a memory table having catch-words as per the claimed invention. The data memory of Davidson is utilized by an associated microprocessor for storing and accessing data

associated with performing the various functions and features programmed in an additional program memory. Applicant respectfully submits that such data is altogether different with regard to content/nature from the herein claimed catch-words. It is simply erroneous to suggest that the catch-words of the memory table as per the claimed invention are akin to a number or a set of menu keys which are simply used for quicker access to all the features of a telecommunication terminal. The Davidson reference teaches, rather, to have a minimum number of menu keys to allow for just a few key presses in order to have complete access to all of the various choices in a higher archical menu of a telecommunication terminal. Conversely, the present invention claims a catch-word based menu structure of a telecommunication terminal wherein a user need only confirm a selected key word so as to reach the allocated option of the menu structure.

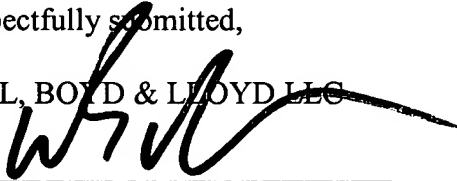
In light of the above-Applicant respectfully submits that independent claim 5 of the present application, as well as claims 6-8 which respectively depend therefrom, are both novel and non-obvious over the art of record. Accordingly, Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

It is further acknowledged that a one month extension of time of \$110.00 is due in connection with this response at this time. If any additional fees are due in connection with this application as a whole, the office is hereby authorized to deduct said fees from Deposit Account No.: 02-1818. If such a deduction is made, please indicate the Attorney Docket Number (0112740-459) on the account statement.

Respectfully submitted,

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